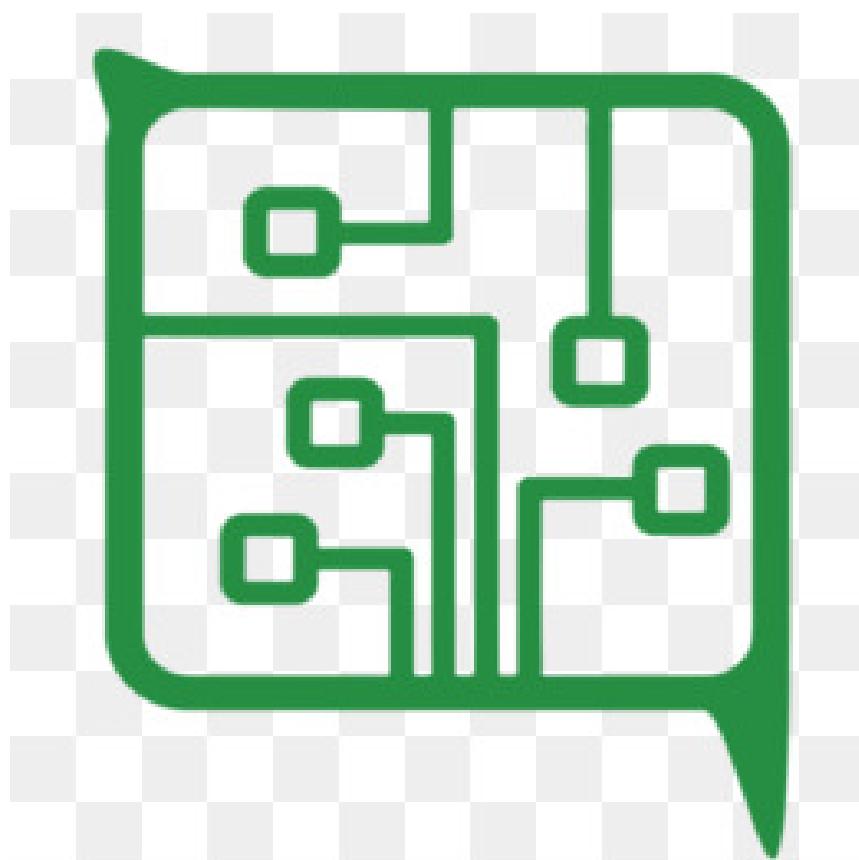


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National Instruments Multisim 14 Full Version 64bit Latest Rar Windows



**DOWNLOAD:** <https://tinurl.com/2itpi1>



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It is a very versatile SPICE simulator and supports a variety of user-defined circuit and SPICE circuit model types. It provides many features such as a multi-dimensional FFT and FFT board for simulation and an Xilinx Virtex-II FPGA-based circuit model to make it easy to target Xilinx FPGA boards. The most significant capabilities of Multisim are:

- \* Support for up to 18 switches per node
- \* Switch accuracy, tracking, and bounce control
- \* Local or remote bus analysis
- \* Clock gating
- \* Clock gating with bus analysis and tuning
- \* Clock stability testing
- \* Multiple independent clock generators
- \* Accuracy verification
- \* User-defined clock bins
- \* User-defined input and output device models
- \* Dynamic simulation
- \* Controlled sources
- \* Control-loop user-defined simulator models
- \* Loop-based floating-point vector math support
- \* User-defined event generators
- \* User-defined transient and analysis
- \* User-defined complex analysis
- \* User-defined digital-signal generators
- \* User-defined unit-delay analysis
- \* User-defined analog functions
- \* Analog filter design
- \* Analog-to-digital converter (ADC) models
- \* OpenCL-based GPUs
- \* OpenCL support for Xilinx, Altera, Intel, ARM, PowerPC, and others
- \* User-defined vector FFT models
- \* User-defined vector FFT boards
- \* User-defined bus analyzer
- \* User-defined bus analyzer board
- \* User-defined FFT board
- \* User-defined analog filter board
- \* User-defined differential pair board
- \* User-defined op amp board
- \* User-defined I/O board
- \* User-defined port board
- \* User-defined SPICE model board
- \* User-defined wave table board
- \* User-defined comparator board
- \* User-defined complex analysis board
- \* User-defined signal-generator board
- \* User-defined waveform simulator
- \* User-defined power supply analysis
- \* User-defined interrupt simulation
- \* Multi-layer multisim simulation
- \* Loop-based thermal simulation

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